

**NOTICE OF APPEAL FROM THE EXAMINER TO THE  
BOARD OF PATENT APPEALS AND INTERFERENCES**

Docket Number  
8008-1052

Applicant(s): Yasutaka NAKASHIBA	Conf.: 2273
Application #: 10/812,282	Filed: March 30, 2004
Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE	
Group Art Unit: 2815	Examiner: Jerome Jackson Jr.

Applicant hereby appeals to the Board of Patent Appeals and Interferences from the decision of the Examiner dated **August 13, 2009** rejecting the following claims: **23-48**.

The fee for this Notice of Appeal is: **\$540.00**

If I have checked one of the boxes below, then this is also a petition for an extension of time under 37 CFR 1.136(a) to reply to the rejection:

Extension of time requested for:	Non-small entity fee for extension of time:
<input type="checkbox"/> - 1 Month	\$130.00
<input type="checkbox"/> - 2 Months	\$490.00
<input type="checkbox"/> - 3 Months	\$1,110.00
<input type="checkbox"/> - 4 Months (if available)	\$1,730.00

The sum of the fee for the Notice of Appeal and the fee for any extension of time is: **\$540.00**

Applicant claims small entity status. See 37 CFR 1.27. Therefore, the fee shown above is reduced by half, and the resulting fee is: **\$**

A check in the amount of the fee is enclosed.

The required filing fees are being paid simultaneously herewith by credit card.

The Commissioner is hereby authorized to charge any fees, deficiency, or credit any overpayment which may be required, to our credit card or Deposit Account No. 25-0120.

Respectfully submitted,

YOUNG & THOMPSON

/Thomas W. Perkins/

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TWP/dp  
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